

Electronic Acknowledgement Receipt

EFS ID:	1439188
Application Number:	10016449
International Application Number:	
Confirmation Number:	5286
Title of Invention:	Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC
First Named Inventor/Applicant Name:	Robert Thomas Bailis
Customer Number:	47052
Filer:	Kelvin M. Vivian
Filer Authorized By:	
Attorney Docket Number:	RPS920010127US1
Receipt Date:	15-JAN-2007
Filing Date:	10-DEC-2001
Time Stamp:	18:01:06
Application Type:	Utility

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Appeal Brief Filed	RPS920010127US1_ReplyBrief.pdf	42160	no	5

Warnings:

Information:	
Total Files Size (in bytes):	42160
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p>	
<p>New Applications Under 35 U.S.C. 111. If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p>	
<p>National Stage of an International Application under 35 U.S.C. 371 If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>	